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<input type="checkbox"/>	L8	L7 and purge	2
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<input type="checkbox"/>	L5	5670218.pn.	1
<input type="checkbox"/>	L4	5926743.pn.	1
<input type="checkbox"/>	L3	L2 and clean\$	1
<input type="checkbox"/>	L2	6183563.pn.	1
<input type="checkbox"/>	L1	5988187.pn.	1

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 20:23:30

Inventor Name Search Result

Your Search was:

Last Name = PARK

First Name = YOUNG-HOON

Application#	Patent#	Status	Date Filed	Title	Inventor Name 23
<u>10464340</u>	Not Issued	030	06/18/2003	IMAGE SENSOR AND METHOD OF FABRICATING THE SAME	PARK, YOUNG- HOON
<u>10461265</u>	Not Issued	071	06/13/2003	CMOS IMAGE SENSOR AND METHOD OF FABRICATING THE SAME	PARK, YOUNG- HOON
<u>10396902</u>	<u>6696353</u>	150	03/25/2003	INTEGRATED CIRCUIT CHIP HAVING ANTI- MOISTURE-ABSORPTION FILM AT EDGE THEREOF AND METHOD OF FORMING ANTI- MOISTURE-ABSORPTION FILM	PARK, YOUNG- HOON
<u>10322455</u>	Not Issued	094	12/19/2002	CORYNEBACTERIUM AMMONIAGENES KCCM 10340 FOR PRODUCING 5'- XANTHYLIC ACID	PARK, YOUNG- HOON
<u>10199538</u>	Not Issued	041	07/19/2002	FUSE AREA STRUCTURE INCLUDING PROTECTION FILM ON SIDEWALL OF FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME	PARK, YOUNG- HOON
<u>10102312</u>	<u>6563162</u>	150	03/19/2002	SEMICONDUCTOR	PARK,

				MEMORY DEVICE FOR REDUCING PARASITIC BIT LINE CAPACITANCE AND METHOD OF FABRICATING THE SAME	YOUNG- HOON
<u>10094571</u>	<u>6573184</u>	150	03/07/2002	APPARATUS AND METHOD FOR DEPOSITING THIN FILM ON WAFER USING ATOMIC LAYER DEPOSITION	PARK, YOUNG- HOON
<u>09971778</u>	<u>6555450</u>	150	10/04/2001	CONTACT FORMING METHOD FOR SEMICONDUCTOR DEVICE	PARK, YOUNG- HOON
<u>09935971</u>	<u>6509255</u>	150	08/23/2001	FUSE AREA STRUCTURE HAVING GUARD RING SURROUNDING FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME	PARK, YOUNG- HOON
<u>09885432</u>	<u>6423589</u>	150	06/20/2001	METHODS FOR FABRICATING CMOS INTEGRATED CIRCUITS INCLUDING SOURCE/DRAIN COMPENSATING REGIONS	PARK, YOUNG- HOON
<u>09848579</u>	<u>6579372</u>	150	05/03/2001	APPARATUS AND METHOD FOR DEPOSITING THIN FILM ON WAFER USING ATOMIC LAYER DEPOSITION	PARK, YOUNG- HOON
<u>09848577</u>	Not Issued	041	05/03/2001	REACTOR FOR DEPOSITING THIN FILM ON WAFER	PARK, YOUNG- HOON
<u>09848533</u>	Not Issued	071	05/03/2001	ATOMIC LAYER DEPOSITION (ALD) THIN FILM DEPOSITION EQUIPMENT HAVING CLEANING APPARATUS AND CLEANING METHOD	PARK, YOUNG- HOON
<u>09826590</u>	<u>6465895</u>	150	04/05/2001	BONDING PAD	PARK,

				STRUCTURES FOR SEMICONDUCTOR DEVICES AND FABRICATION METHODS THEREOF	YOUNG-HOON
<u>09739186</u>	<u>6448113</u>	150	12/18/2000	FUSE AREA STRUCTURE INCLUDING PROTECTION FILM ON SIDEWALL OF FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME	PARK, YOUNG-HOON
<u>09719593</u>	<u>6616754</u>	150	12/12/2000	SEGREGATION REDUCING AGENT CONSISTING OF CURDLAN AND ALKALINE SOLUTION AND HYDRAULIC COMPOSITION CONTAINING THE SEGREGATION REDUCING AGENT	PARK, YOUNG-HOON
<u>09715679</u>	<u>6507086</u>	150	11/17/2000	FUSE AREA STRUCTURE HAVING GUARD RING SURROUNDING FUSE OPENING IN SEMICONDUCTOR DEVICE AND METHOD OF FORMING THE SAME	PARK, YOUNG-HOON
<u>09715372</u>	<u>6566735</u>	150	11/17/2000	INTEGRATED CIRCUIT CHIP HAVING ANTI-MOISTURE-ABSORPTION FILM AT EDGE THEREOF AND METHOD OF FORMING ANTI-MOISTURE-ABSORPTION FILM	PARK, YOUNG-HOON
<u>09710953</u>	<u>6509263</u>	150	11/14/2000	METHOD FOR FABRICATING A SEMICONDUCTOR MEMORY DEVICE HAVING POLYSILICON WITH AN ENHANCED SURFACE CONCENTRATION AND	PARK, YOUNG-HOON

				REDUCED CONTACT RESISTANCE	
<u>09479049</u>	<u>6176906</u>	150	01/07/2000	QUATERNARY AMMONIUM SALT FOR PHOTO-CURABLE ANTIFOGGING COMPOSITION, METHOD FOR PREPARING THE SAME, AND PHOTO-CURABLE ANTIFOGGING COMPOSITION	PARK, YOUNG-HOON
<u>09088784</u>	<u>6096925</u>	150	06/01/1998	QUATERNARY AMMONIUM SALT FOR PHOTO-CURABLE ANTIFOGGING COMPOSITION, METHOD FOR PREPARING THE SAME, AND PHOTO-CURABLE ANTIFOGGING COMPOSITION	PARK, YOUNG-HOON
<u>08855024</u>	<u>6274914</u>	150	05/13/1997	CMOS INTEGRATED CIRCUITS INCLUDING SOURCE/DRAIN PLUG	PARK, YOUNG-HOON
<u>08440954</u>	<u>5545578</u>	150	05/15/1995	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE HAVING A LOW-RESISTANCE GATE ELECTRODE	PARK, YOUNG-HOON

Inventor Search Completed: No Records to Display.

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